

## Senior Technical Lead Analog & Mixed Signal Design engineer

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Company: Chipright

Location: Belgium

Category: computer-and-mathematical

## Senior Analog & Mixed Signal Design Engineer with Image Sensor

### Experience Requirements:

Minimum 5 years' experience as an IC Layout Designer with strong Analog/ Mixed Signal Layout skills

Must have vast experience in defining architecture of the image sensor and the Analog chain

Experience in building block specifications and following up on building block layout with other team members

Relevant Experience in coaching a team guiding the layout to successful tape-out

Experience in defining the test plan to characterize the image sensor

Must support the characterization of the sensor in the lab

Must be able to prepare and report technical progress to both the project manager and customer

Experience using Cadence layout tools is required (Virtuoso, PVS DRC & LVS, QRC extraction)

Ability to debug and resolve LVS/DRC errors independently

Experience with advanced CMOS nodes is a plus.

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